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APPLICANTS
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** CONTINUING DATA *****
 (None) LT

** FOREIGN APPLICATIONS *****
 REPUBLIC OF KOREA 10-2003-0086391 12/30/2003
 LT

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 ** 04/28/2004

Foreign Priority claimed <input checked="" type="checkbox"/> yes <input type="checkbox"/> no 35 USC 119 (a-d) conditions met <input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after Allowance Verified and Acknowledged <u>Chenman</u> <u>LT</u> Examiner's Signature Initials	STATE OR COUNTRY KOREA, REPUBLIC OF	SHEETS DRAWING 7	TOTAL CLAIMS 8	INDEPENDENT CLAIMS 2
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TITLE
 Methods of manufacturing transistors using dummy gate patterns

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